

SESSION 1

- 09:00 - 10:00 Hagen Völzer **Express invited talk**
When a System is Fairly Correct
- 10:00 - 10:30 Diletta Cacciagrano, Flavio Corradini, Catuscia Palamidessi
Fair Pi

10:30 - 11:00 COFFEE BREAK

SESSION 2

- 11:00 - 11:30 Jos Baeten, Flavio Corradini, Clemens Grabmayer
On the Star Height of Regular Expressions under Bisimulation
- 11:30 - 12:00 Xu Wang, Marta Kwiatkowska
Compositional State Space Reduction Using Untangled Actions
- 12:00 - 12:30 Ahmed Bouajjani, Jan Strejček, Tayssir Touili
On Symbolic Verification of Weakly Extended PAD

12:30 - 14:30 LUNCH BREAK

SESSION 3

- 14:30 - 15:30 Robin Milner **Joint Express-Infinity-SOS invited talk**
Bigraphs, Multi-local Names and Confluence
- 15:30 - 16:00 Vincent Danos, Jean Krivine, Pawel Sobocinski
General Reversibility

16:00 - 16:30 COFFEE BREAK

SESSION 4

- 16:30 - 17:00 Daniele Gorla
Synchrony vs Asynchrony in Communication Primitives
- 17:00 - 17:30 Lucy Saunders-Evans, Glynn Winskel
Event Structure Spans for Non-deterministic Dataflow
- 17:30 - 18:00 Luís Caires, Hugo Torres Vieira
Extensionality of Spatial Observations in Distributed Systems
- 18:00 - 18:30 Daniel Hirschhoff, Damien Pous
On Closure under Substitution of Strong Bisimilarity

09:20 - 09:30 WELCOME

SESSION 1

09:30 - 10:30 Bartek Klin **invited talk**
Bialgebraic methods in structural operational semantics

10:30 - 11:00 COFFEE BREAK

SESSION 2

11:00 - 11:30 MohammadReza Mousavi, Michel A. Reniers
On well-foundedness and expressiveness of promoted tyft

11:30 - 12:00 Christiano Braga, Alberto Verdejo
Modular SOS with strategies

12:00 - 12:30 Adrian Pop, Peter Fritzon
An Eclipse-based integrated environment for developing executable structural operational semantics specifications (tool demonstration)

12:30 - 14:30 LUNCH BREAK

SESSION 3

14:30 - 15:30 Robin Milner **Joint Express-Infinity-SOS invited talk**
Bigraphs, multi-local names and confluence

16:00 - 16:30 COFFEE BREAK

SESSION 4

16:30 - 17:00 Henrik Pilegaard, Flemming Nielson, Hanne Riis Nielson
Active evaluation contexts for reaction semantics

17:00 - 17:30 Simone Tini
Notes on probabilistic bisimulations

17:30 - 18:00 Vincent Danos, Jean Krivine, Fabien Tarissan
Self-assembling trees

19:30 WORKSHOP DINNER
Please contact the workshop organisers in advance if wishing to attend

09:00 - 09:30	REGISTRATION - WELCOME
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SESSION 1	SOFTWARE VERIFICATION (INVITED LECTURE)
09:30-10:30	Andrey Rybalchenko (with B. Cook and A. Podelski) <i>Termination Proofs for Systems Code</i>

10:30 - 11:00	COFFEE BREAK
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SESSION 2	REACHABILITY ANALYSIS, BISIMULATION
11:00 - 11:30	Giorgio Delzanno, Roberto Montagna <i>On the Reachability Problem for Fragments of Mobile Ambients with Name Restriction</i>
11:30 - 12:00	Mihaela Sighireanu, Tayssir Touili <i>Bounded Communication Reachability Analysis of O-Process Rewrite Systems</i>
12:00 - 12:30	Sibylle Froeschle, Slawomir Lasota <i>Normed Processes, Unique Decomposition, and Complexity of Bisimulation Equivalences</i>

12:30 - 14:30	LUNCH BREAK
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SESSION 3	PROCESS CALCULI, CONCURRENCY
14:30 - 15:30	Robin Milner Joint Express-Infinity-SOS invited talk <i>Bigraphs, Multi-local Names and Confluence</i>
15:30 - 16:00	David Teller <i>Concurrent memory management with the pi-calculus</i>

16:00 - 16:30	COFFEE BREAK
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SESSION 4	WELL-STRUCTURED SYSTEMS
16:30 - 17:00	Nathalie Bertrand (with Ch. Baier and Ph. Schnoebelen) <i>On Computing Fixpoints in Well-Structured Regular Model Checking, with Applications to Lossy Channel Systems</i>
17:00 - 17:30	Gilles Geeraerts (with J-F. Raskin and L. van Begin) <i>An Efficient Algorithm to Compute the Coverability Set of Petri Nets</i>

17:30 - 17:45	BREAK
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SESSION 5	LEARNING (INVITED LECTURE)
17:45 - 18:45	Mahesh Viswanathan <i>Learning to Verify</i>

SESSION 1 INVITED TALK

09:00 - 10:00 Anna Slobodova

Challenges for Formal Verification in Industrial Setting

10:00 - 10:30 M. Siegle, K. Lampka, M. Walter

An easy-to-use, efficient tool-chain to analyze the availability of tele-communication equipment

10:30 - 11:00 COFFEE BREAK

SESSION 2 HANDLING LARGE STATE SPACES

11:00 - 11:30 M. Weber, M. Hammer

"To Store or Not To Store" Reloaded: Reclaiming Memory on Demand

11:30 - 12:00 H. Saidi

Discovering Symmetries

12:00 - 12:30 L. Brim, I. Cerna, P. Moravec, J. Simsa

On Combining Partial Order Reduction with Fairness Assumptions

12:30 - 14:30 LUNCH BREAK

SESSION 3 MODEL BASED TESTING

14:30 - 15:00 F. Maraninchi, C. Helmstetter, L. Maillet-Contoz

Test Coverage for Loose Timing Annotations

15:00 - 15:30 A. Hessel, P. Pettersson

Model Based Testing of a WAP Gateway

15:30 - 16:00 T. Willemse

Heuristics for ioco-Based Test-Based Modelling

16:00 - 16:30 COFFEE BREAK

SESSION 4 CASE STUDIES I

16:30 - 17:00 A. Smrcka, P. Matousek, Z. Rehak, D. Safranek, T. Vojnar, V. Rehak

Verifying VHDL Designs with Multiple Clocks in SMV

17:00 - 17:30 A. Mathijssen, A. J. Pretorius

Verified Design of an Automated Parking Garage

17:30 - 18:00 S. Gilmore, A. Clark

Evaluating quality of service for service level agreements

19:30 CONFERENCE DINNER

SESSION 1 INVITED TALK

09:30 - 10:30 Edward Lee **Joint invited talk with CONCUR**
Making Concurrency Mainstream

10:30 - 11:00 COFFEE BREAK

SESSION 2 CASE STUDIES II

11:00 - 11:30 P. Cuijpers, A. Fyukov
Simulation-based Performance Analysis of a Medical Image-Processing Architecture

11:30 - 12:00 J. T. Muehlberg, G. Luetzgen
BLASTing Linux Code

12:00 - 12:30 S. Roy, I. Sahal
A Finite State Modelling of AFDX Frame Management using Spin

12:30 - 14:30 LUNCH BREAK

SESSION 3 FORMAL DESCRIPTION AND ANALYZIS TECHNIQUES

14:30 - 15:00 H. Fecher, J. Schoenborn
UML 2.0 State Machines: Complete Formal Semantics via Core State Machines

15:00 - 15:30 B. Bonakdarpour, S. S. Kulkarni
Automated Incremental Synthesis of Timed Automata

15:30 - 16:00 W. Zhang
SAT-based Verification of LTL formulas

16:00 - 16:30 COFFEE BREAK

SESSION 4 TOOLS SESSION

16:30 - 17:00 T. Wahls, B. Krause
jmle: A Tool for Executing JML Specifications via Constraint Programming

17:00 - 17:30 R. Huuck, A. Fehnker, F. Rauch, M. Lussenburg, P. Jayet
Goanna – A Static Model Checker

17:30 - 18:00 T. Margaria
jETI repository of verification tools

18:00 - 19:00 FMICS BUSINESS MEETING

09:20 - 09:30 WELCOME

SESSION 1

09:40 - 10:30 Viorica Sofronie-Stokermann
Sheaves and Geometric logic in the study of Concurrency

10:30 - 11:00 COFFEE BREAK

SESSION 2

11:00 - 11:45 Thomas Kahl
Relative directed homotopy of Partially ordered spaces

11:45 - 12:30 Luigi Santocanale
Topological properties of Events structures

12:30 - 14:30 LUNCH BREAK

SESSION 3

14:30 - 15:15 Sanjeevi Krishnan
Homotopies on Continuous lattices

15:15 - 16:00 Emmanuel Haucourt **tutorial**
Fundamental categories and Components of Partially ordered spaces

16:00 - 16:30 COFFEE BREAK
